A Low-Complexity, High Accuracy Model of a CPU Anti-Resonance System

Abstract

A low-complexity, high accuracy model of a CPU anti-resonance system has been developed. The model includes a load model that simulates the performance of the anti-resonance circuit, a transistor that models the performance of a high frequency capacitor, and a capacitor that models the performance of the intrinsic capacitance of a section of the microprocessor. All of the elements of the model are connected in parallel.

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